

Notice of References Cited

Application/Control No.
09/822,532

Applicant(s)/Patent Under Reexam
Lee

Examiner
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Art Unit
1765

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NON-PATENT DOCUMENTS

	Include, as applicable: Author, Title, Date, Publisher, Edition or Volume, Pertinent Pages
U	S. Wolf, "Silicon Processing for the VLSI Era", Lattice Press, Volume 1, 1986, pp. 589.
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¹ A copy of this reference is not being furnished with this Office action. See MPEP § 707.05(a).

¹ Dates in MM-YYYY format are publication dates.

² Classifications may be U.S. or foreign.